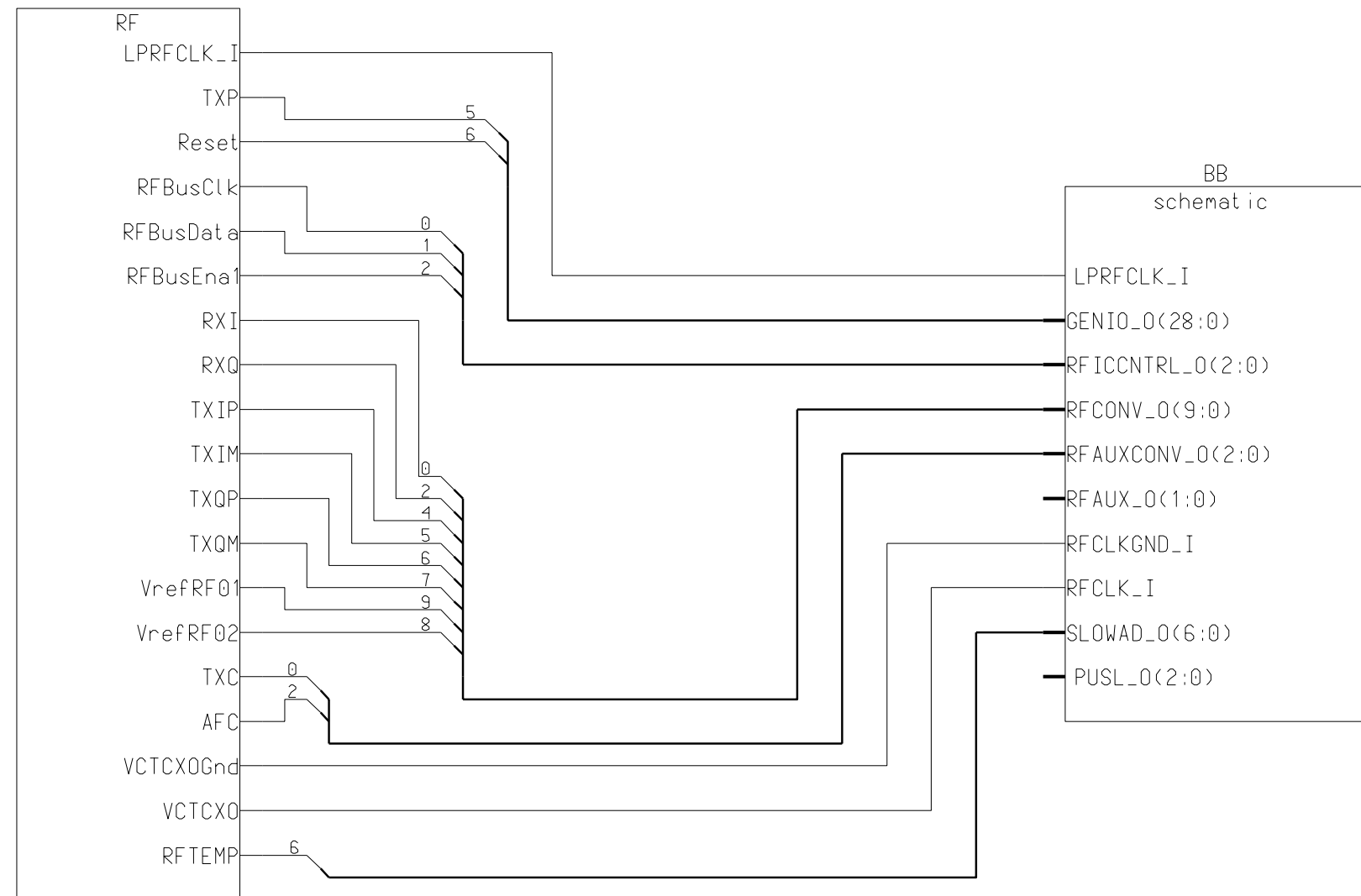
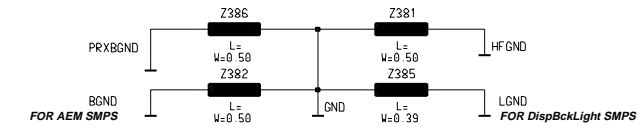


RF-BB connection diagram

GLOBAL SYMBOLS	
VR1A	
VR2	
VR3	
VR4	
VR5	
VR6	
VR7	
VBAT	
IPA1	Not used
IPA2	Not used

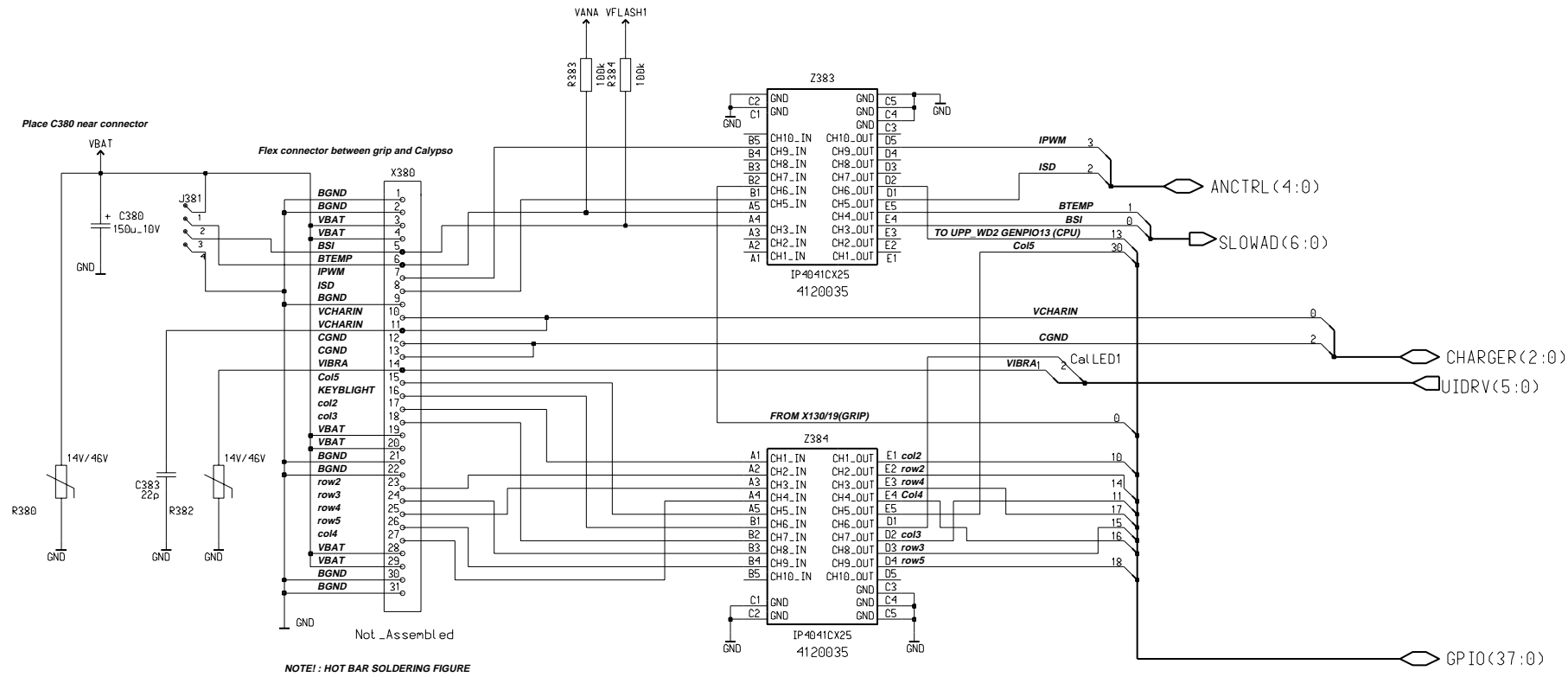


System Connector Diagram

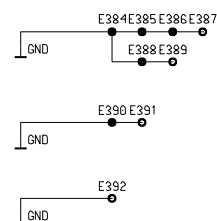
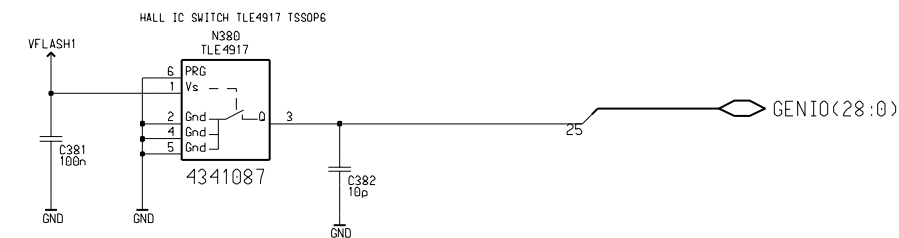


- ◇ DSP_MCUTEST(2:0)
- ◇ MBUS
- ◇ FBUS(1:0)
- ◇ RFAUX(1:0)

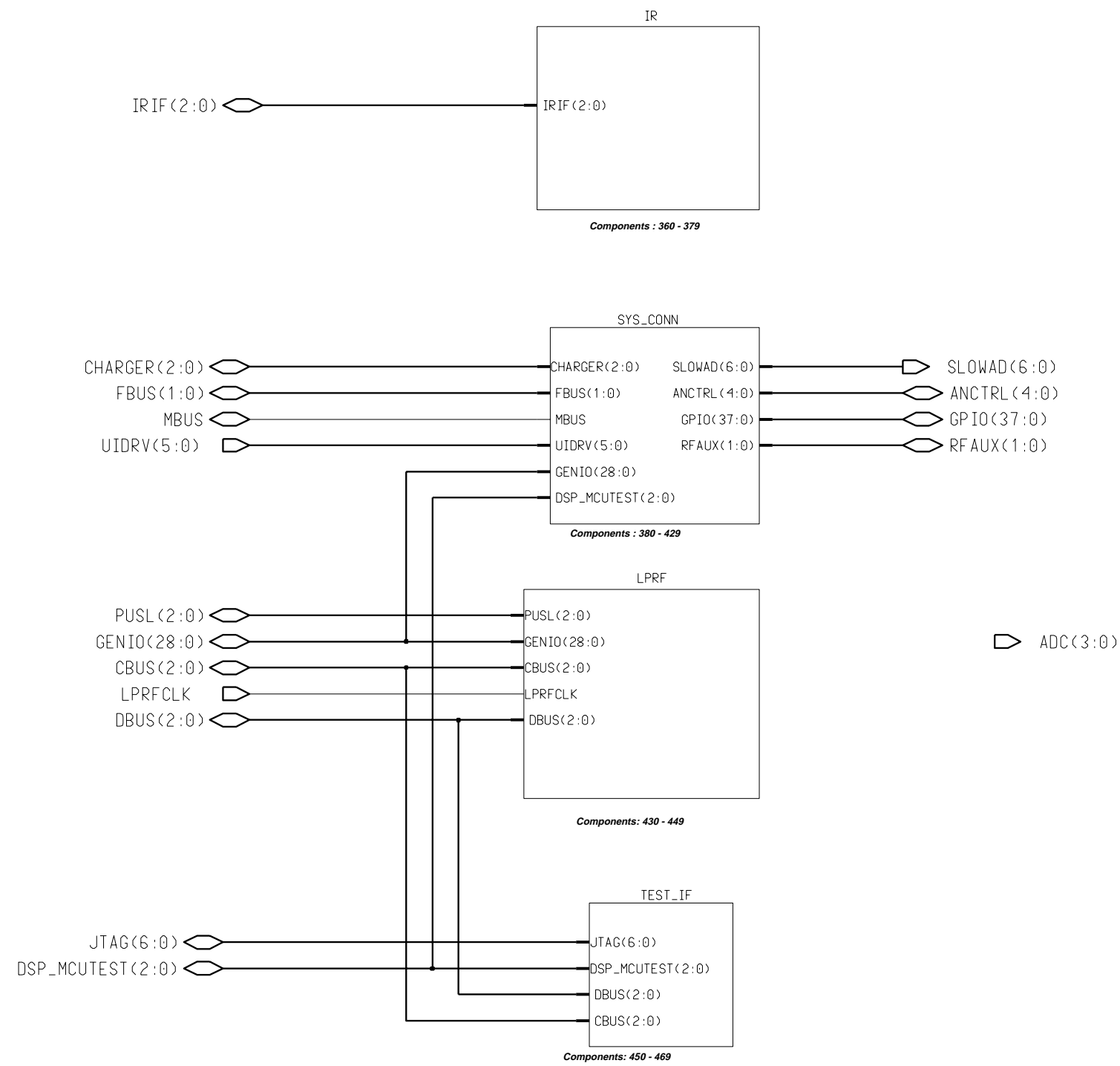
Grounds are connected together near grip connector








Note 1: These Are ScrewHoles

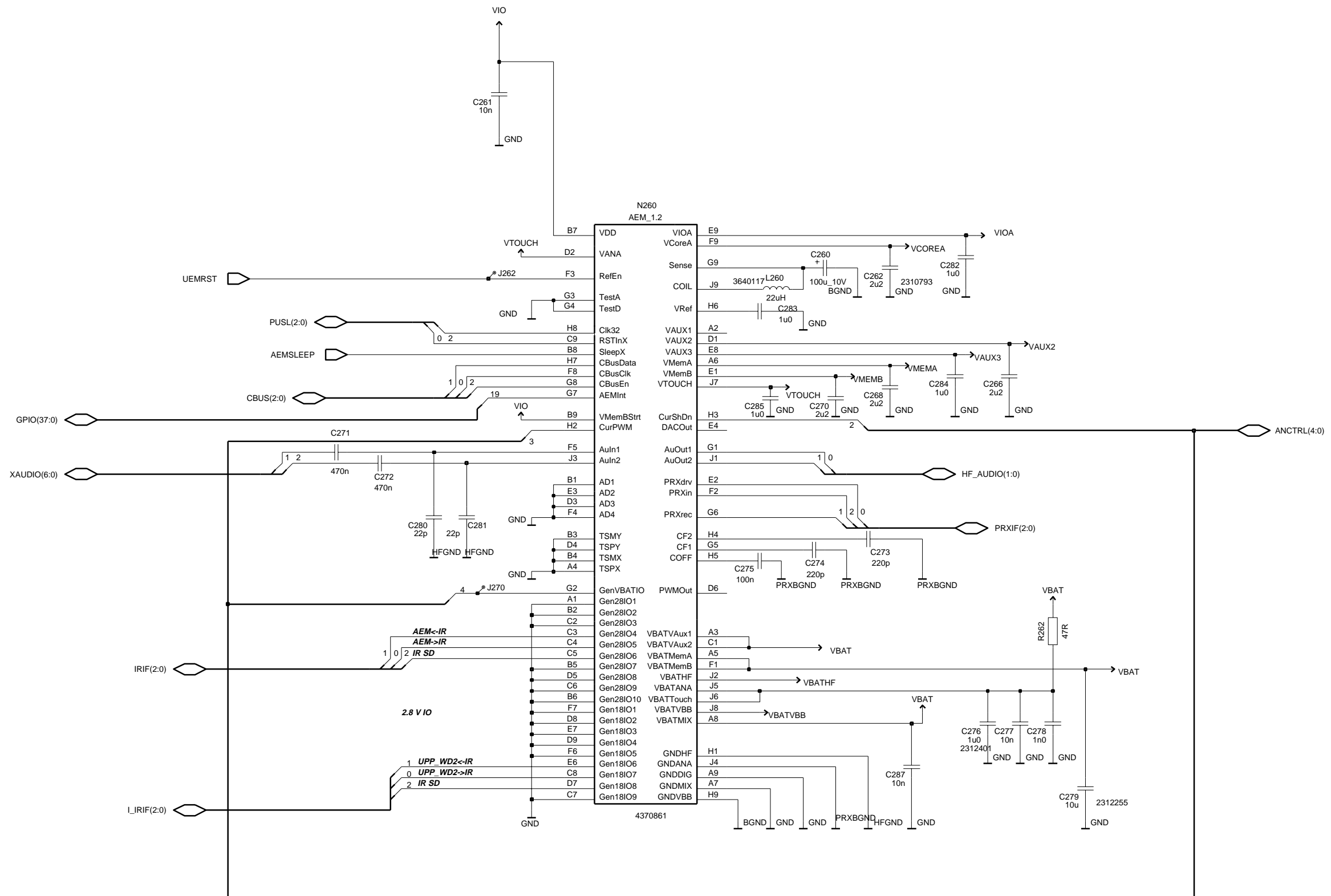


Accessories interface diagram

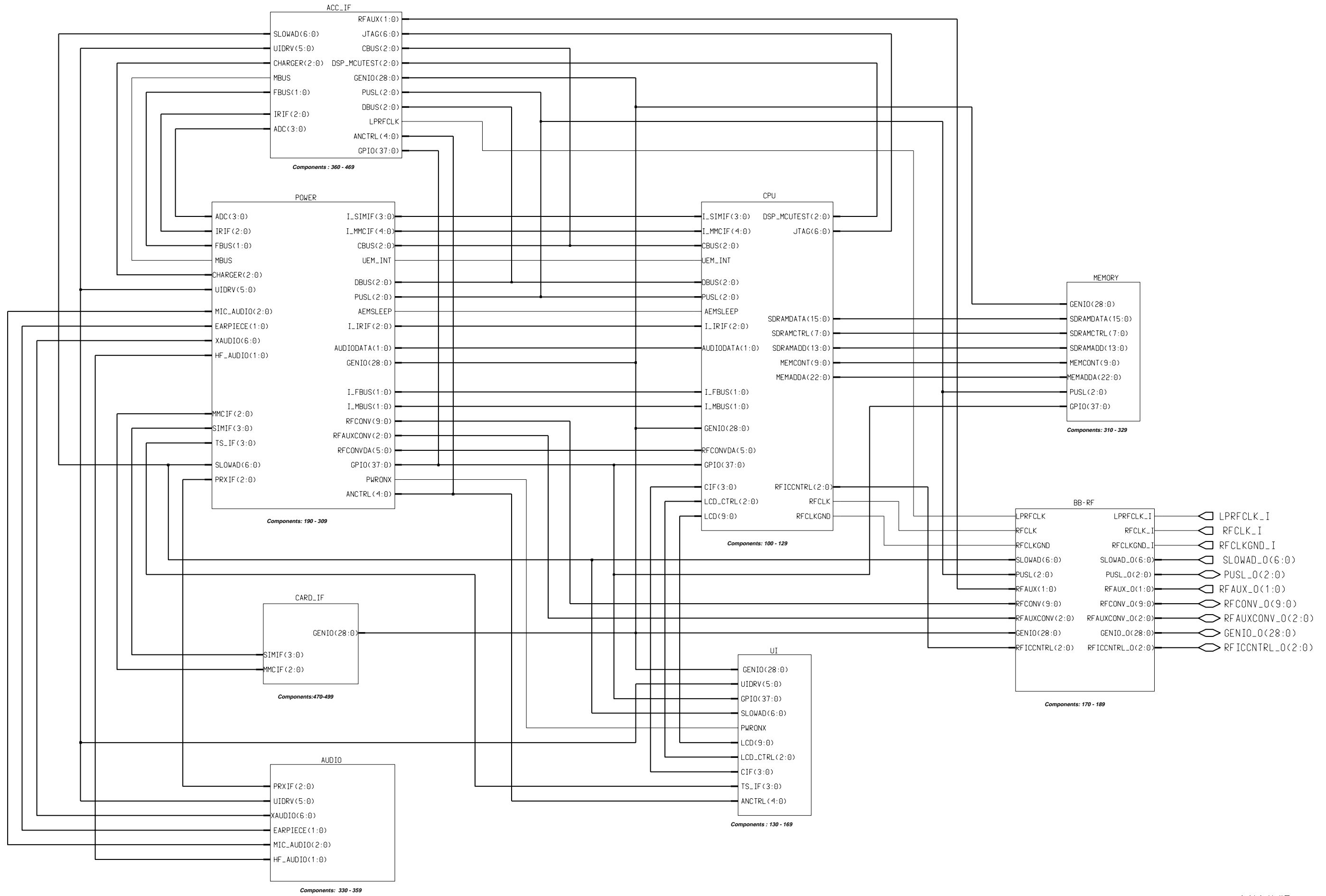


AEM diagram

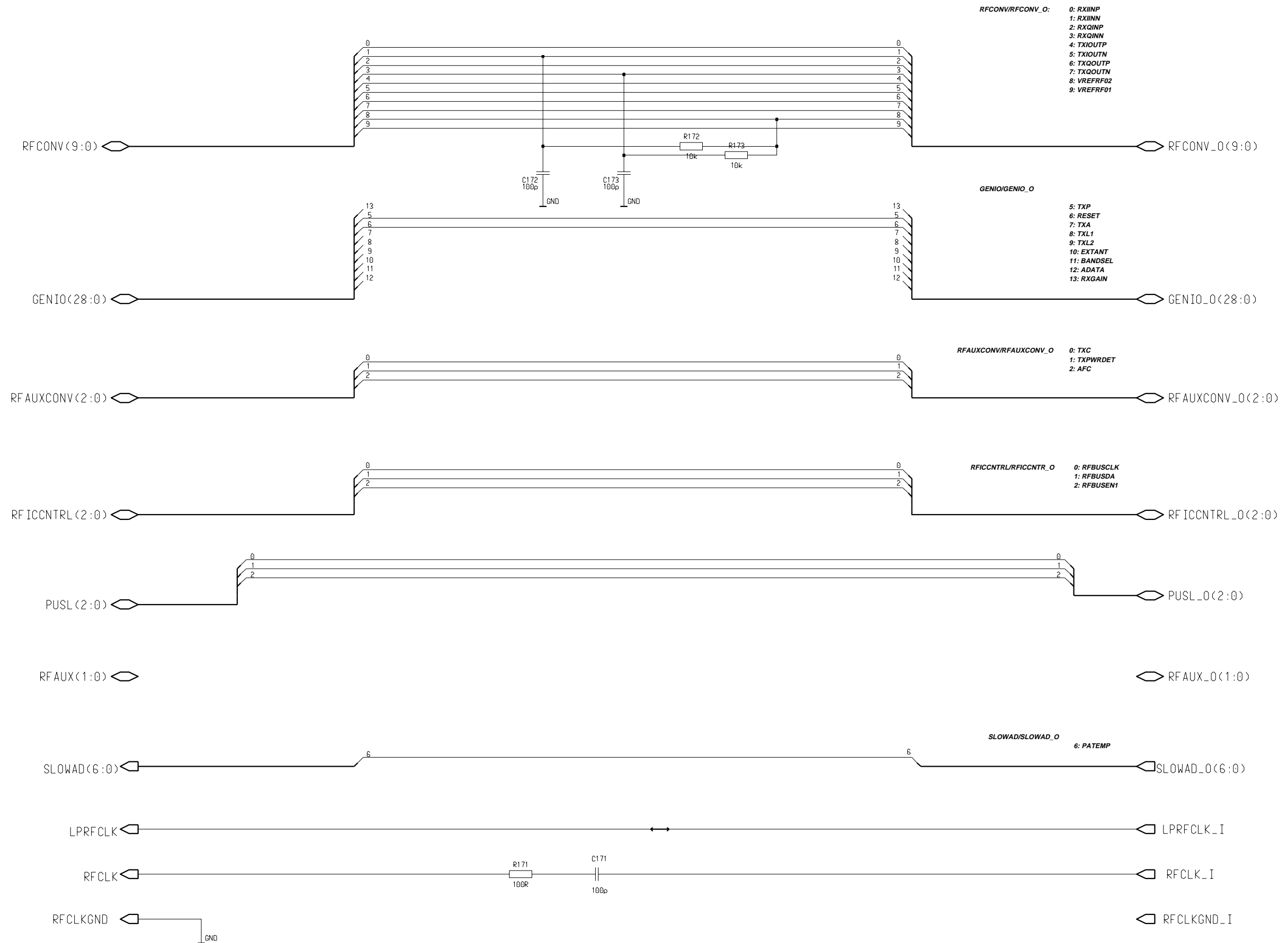
-  TS_IF(3:0)
-  MMCIF(2:0)
-  L_MMCIF(4:0)
-  ADC(3:0)
-  GENIO(28:0)



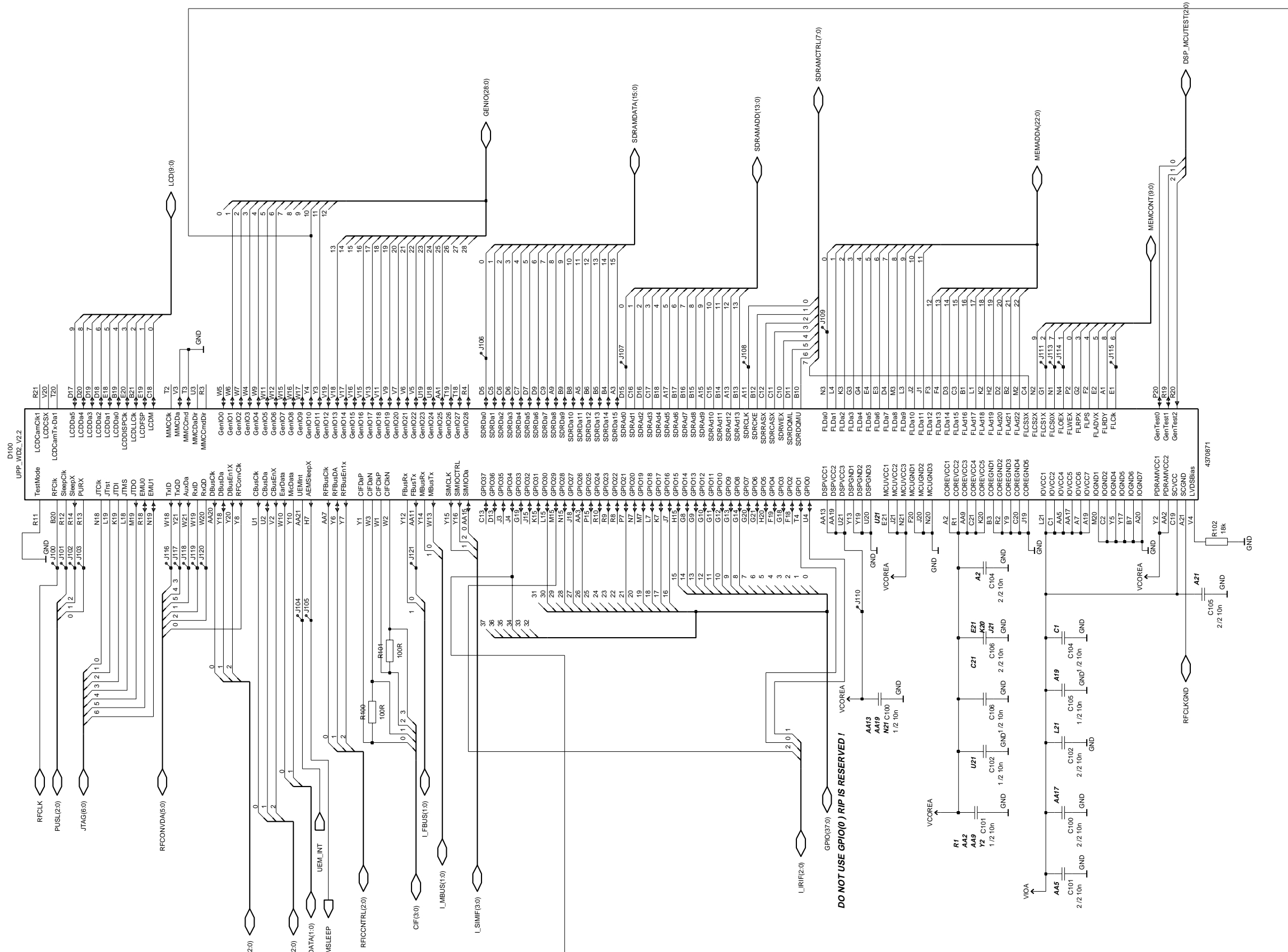
Baseband Diagram



BB-RF Interface diagram

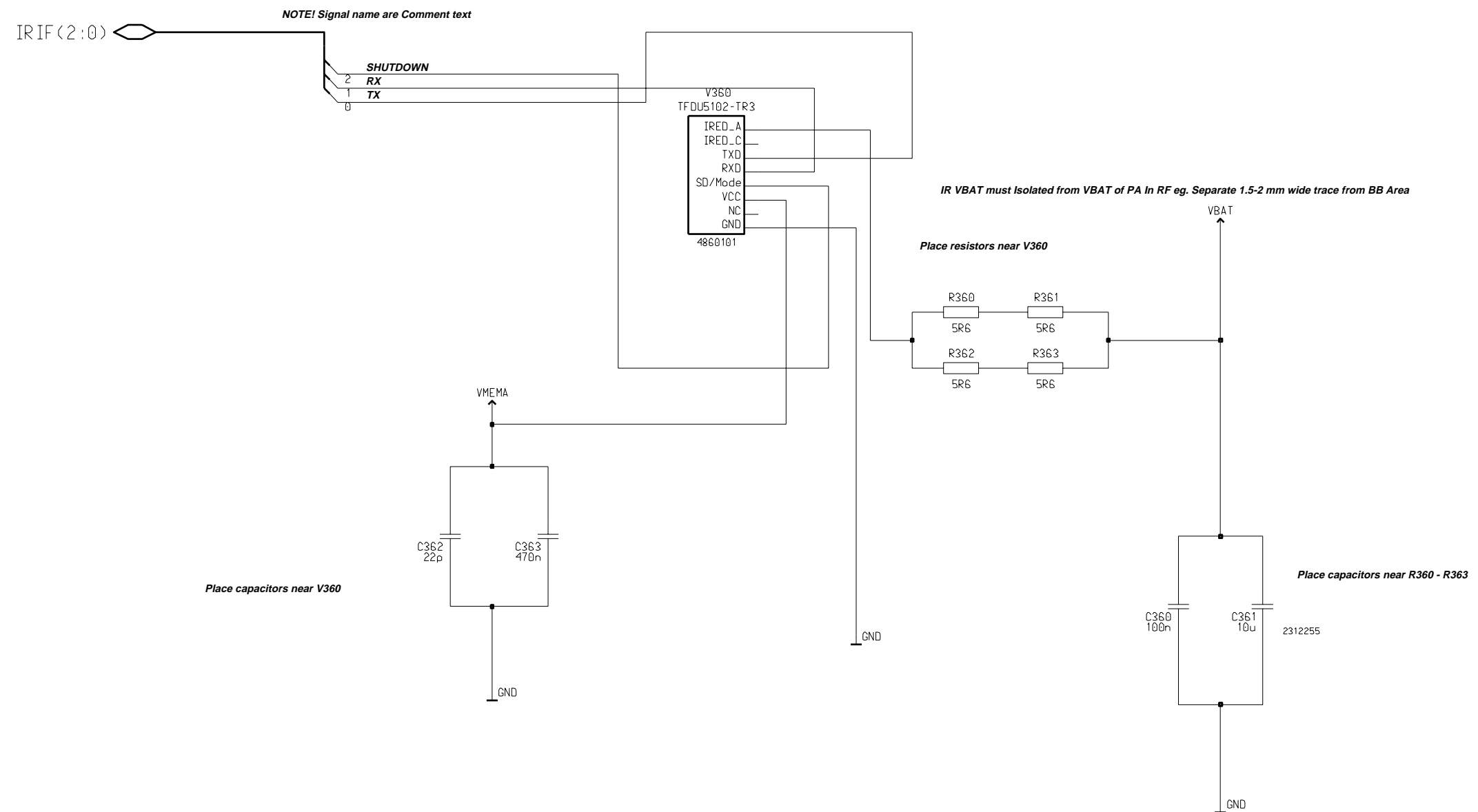


CPU Diagram

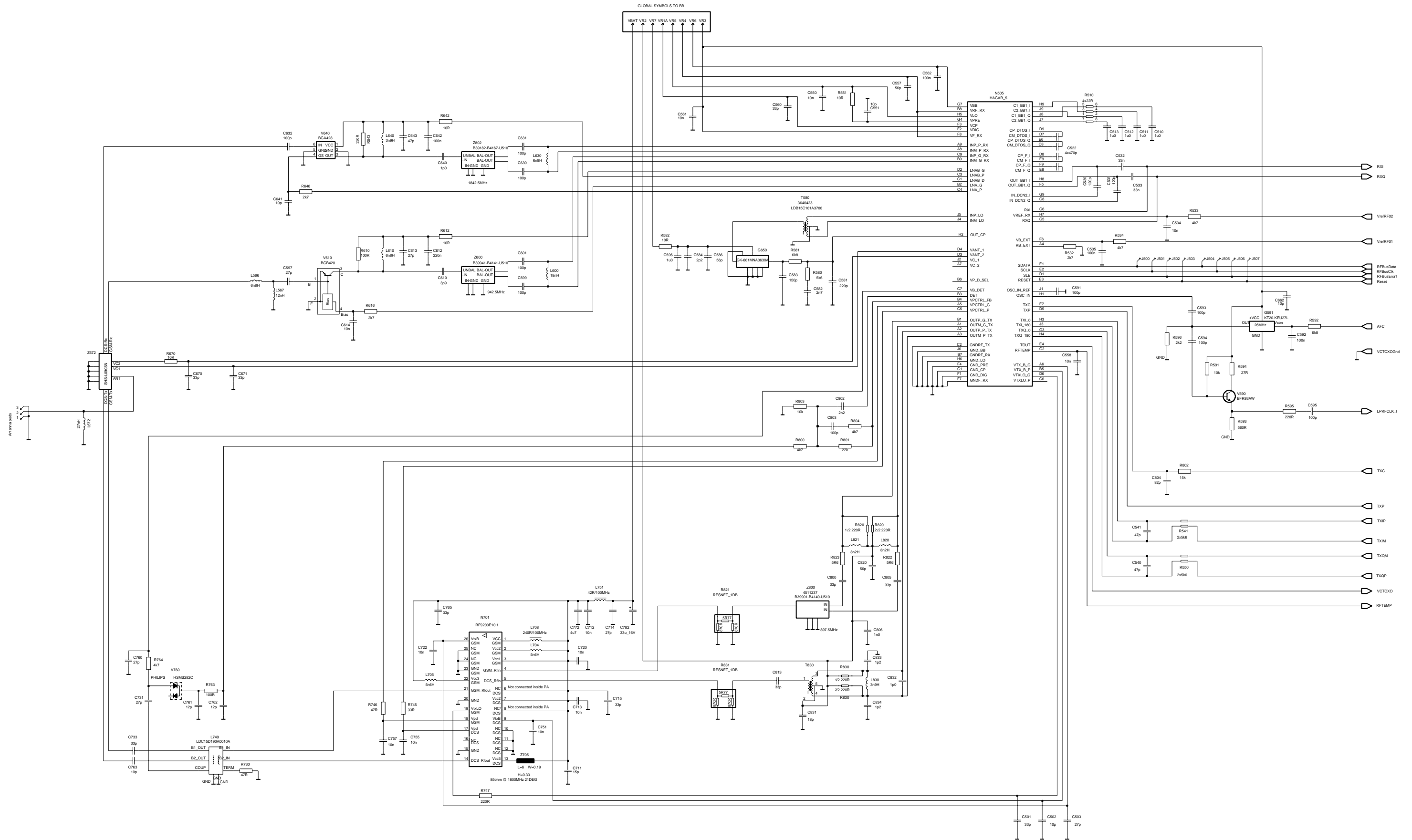


DO NOT USE GPIO(0) / RIP IS RESERVED!

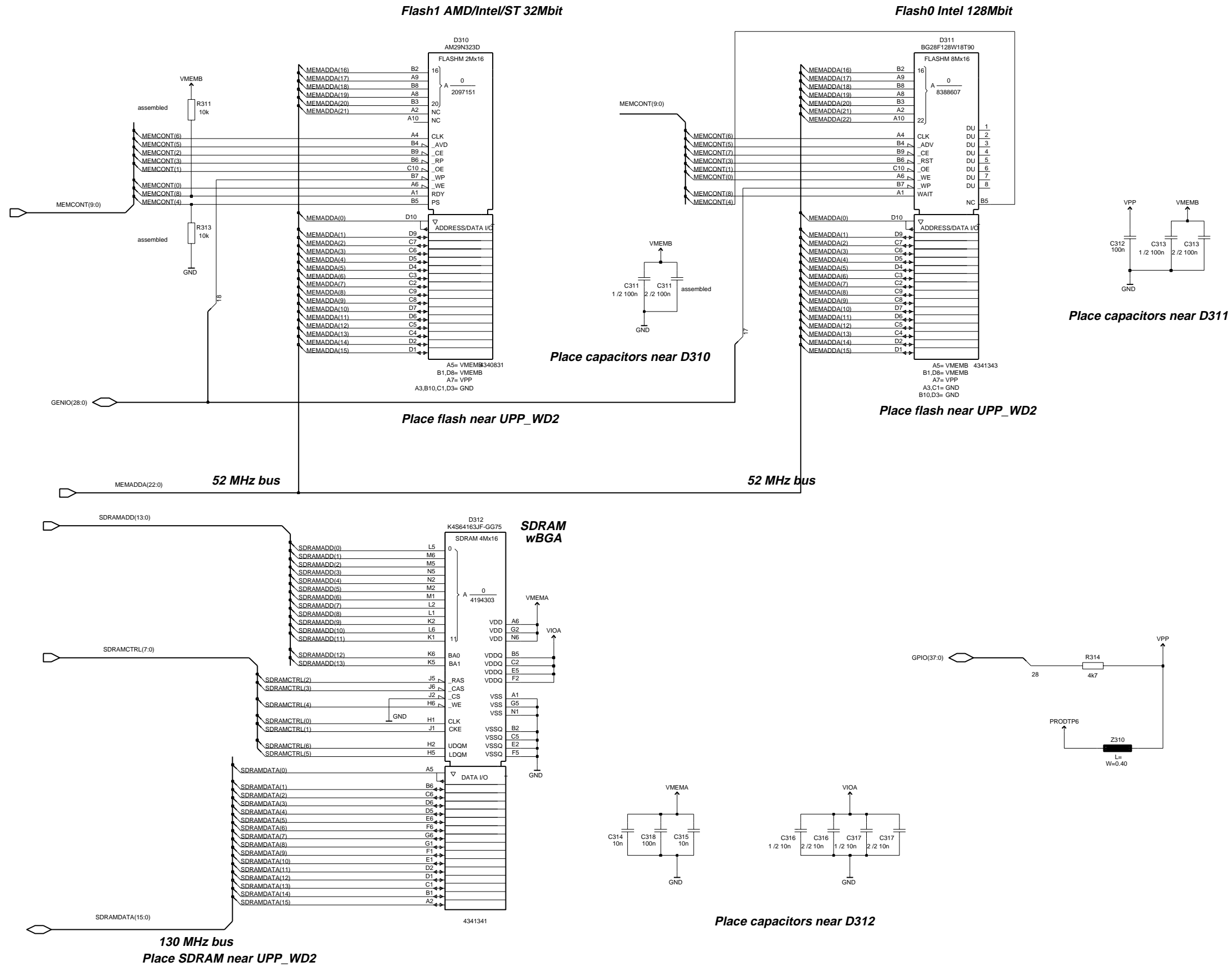
IR module diagram



RF diagram

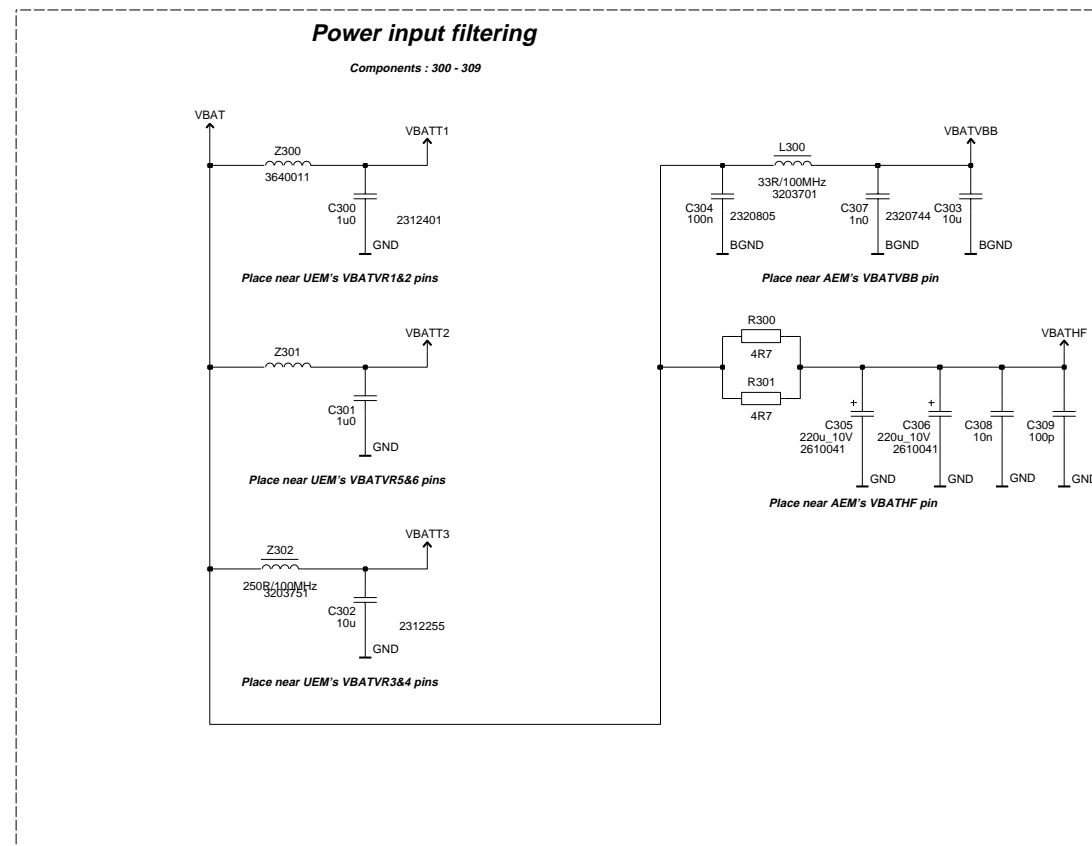
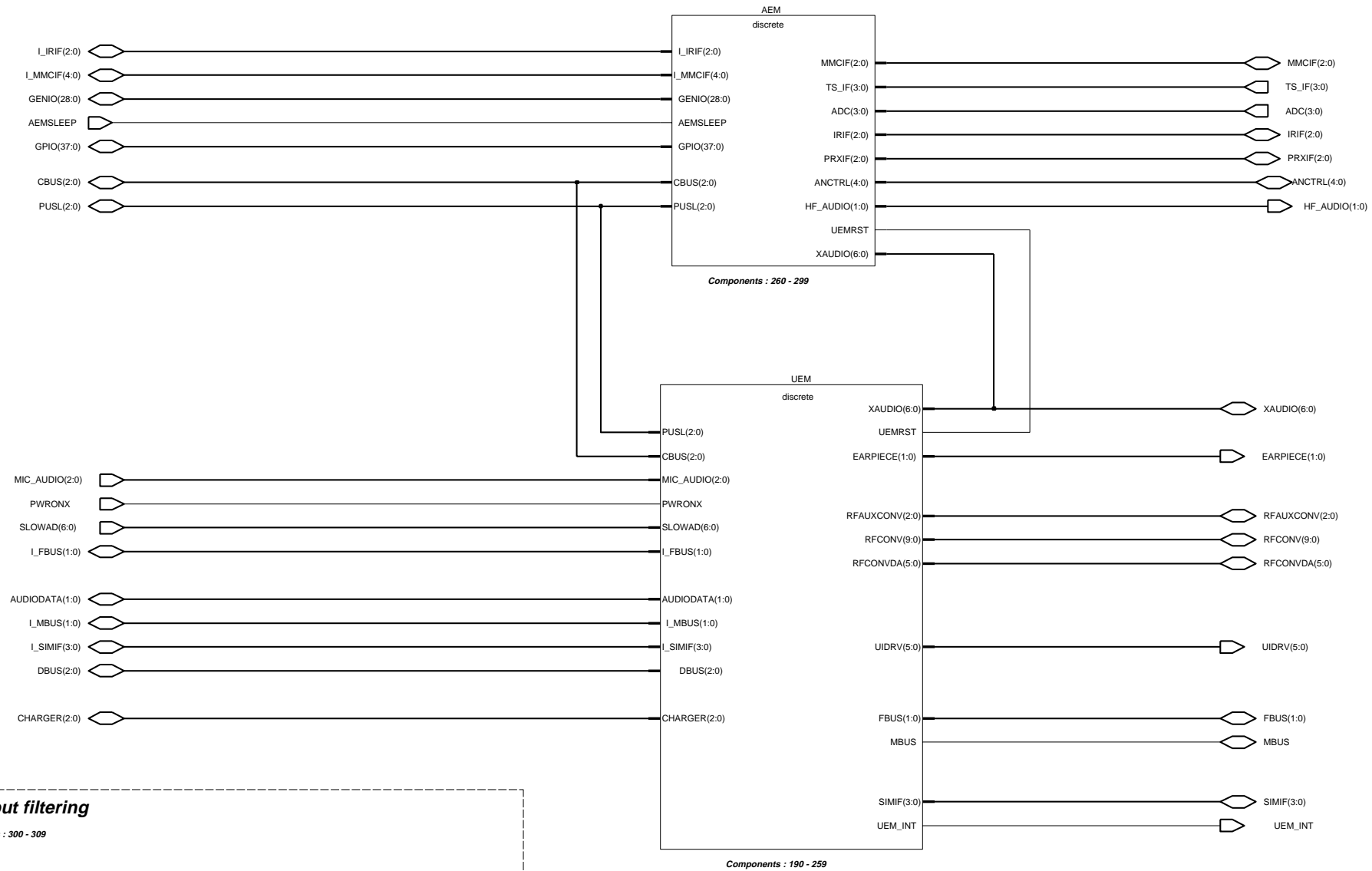


Memories diagram



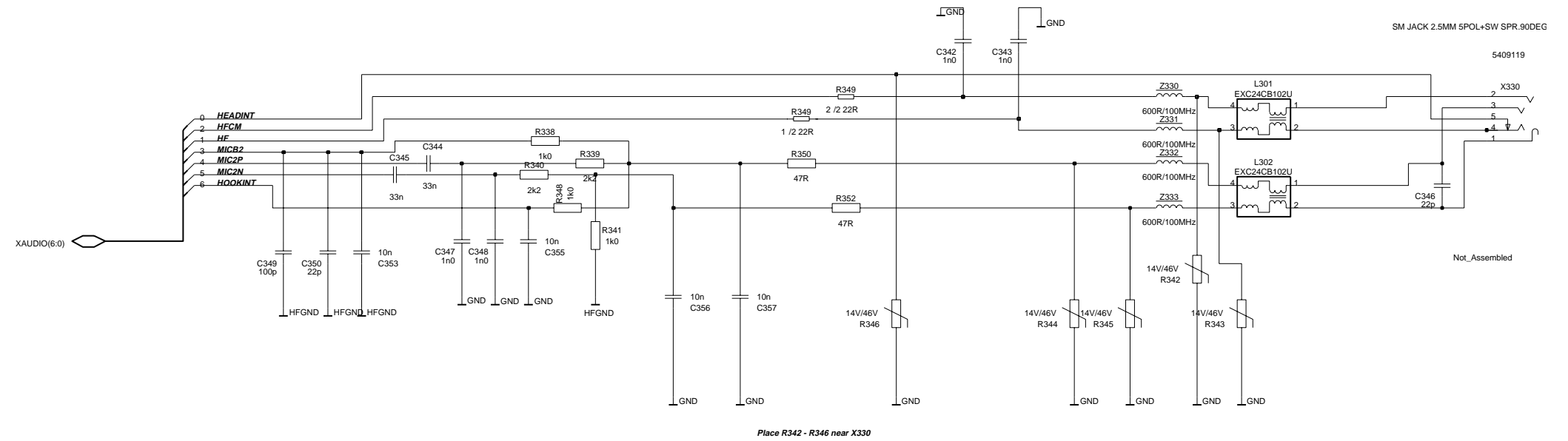
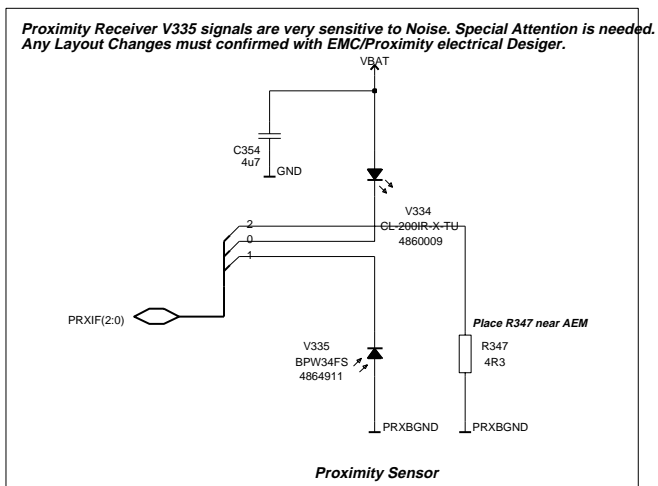
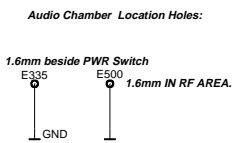
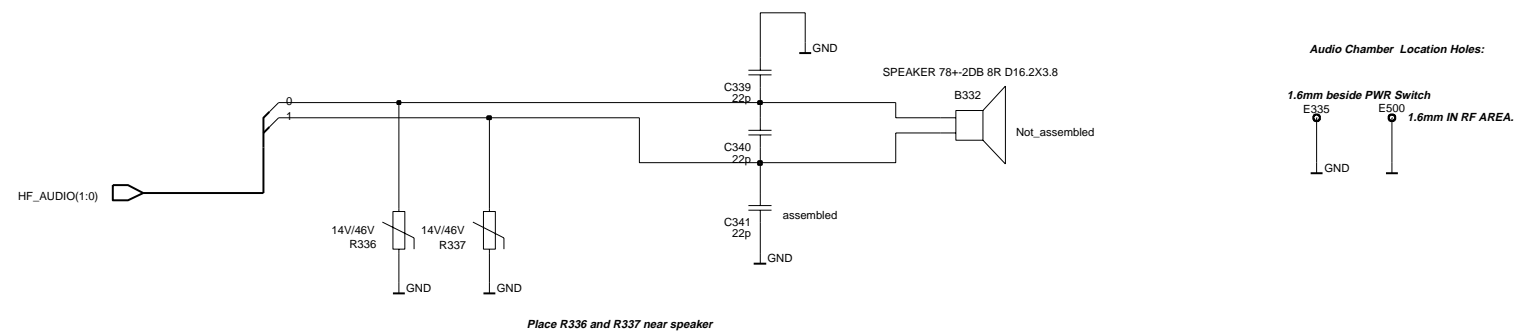
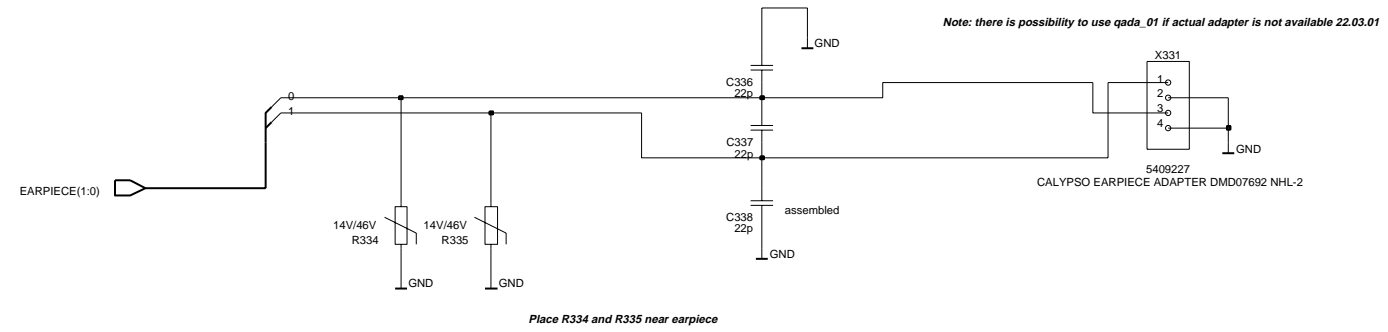
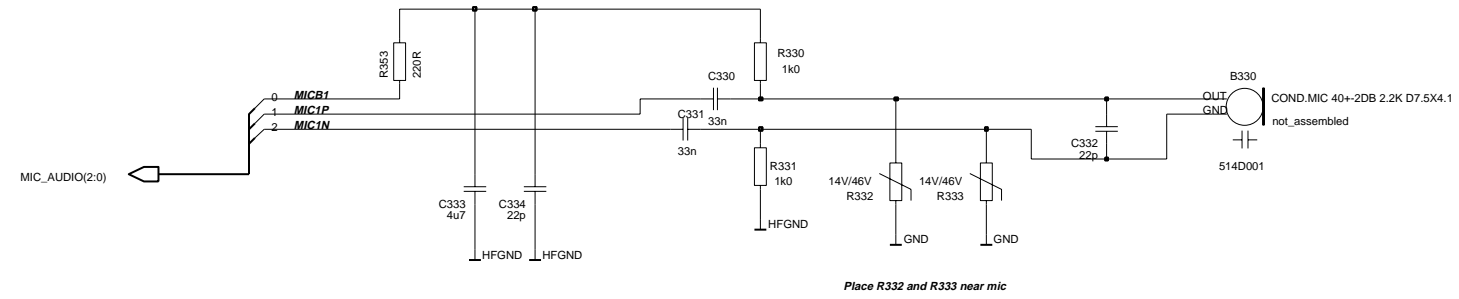
PUSL(2:0)

Power Diagram



Audio

UIDRV(5:0)

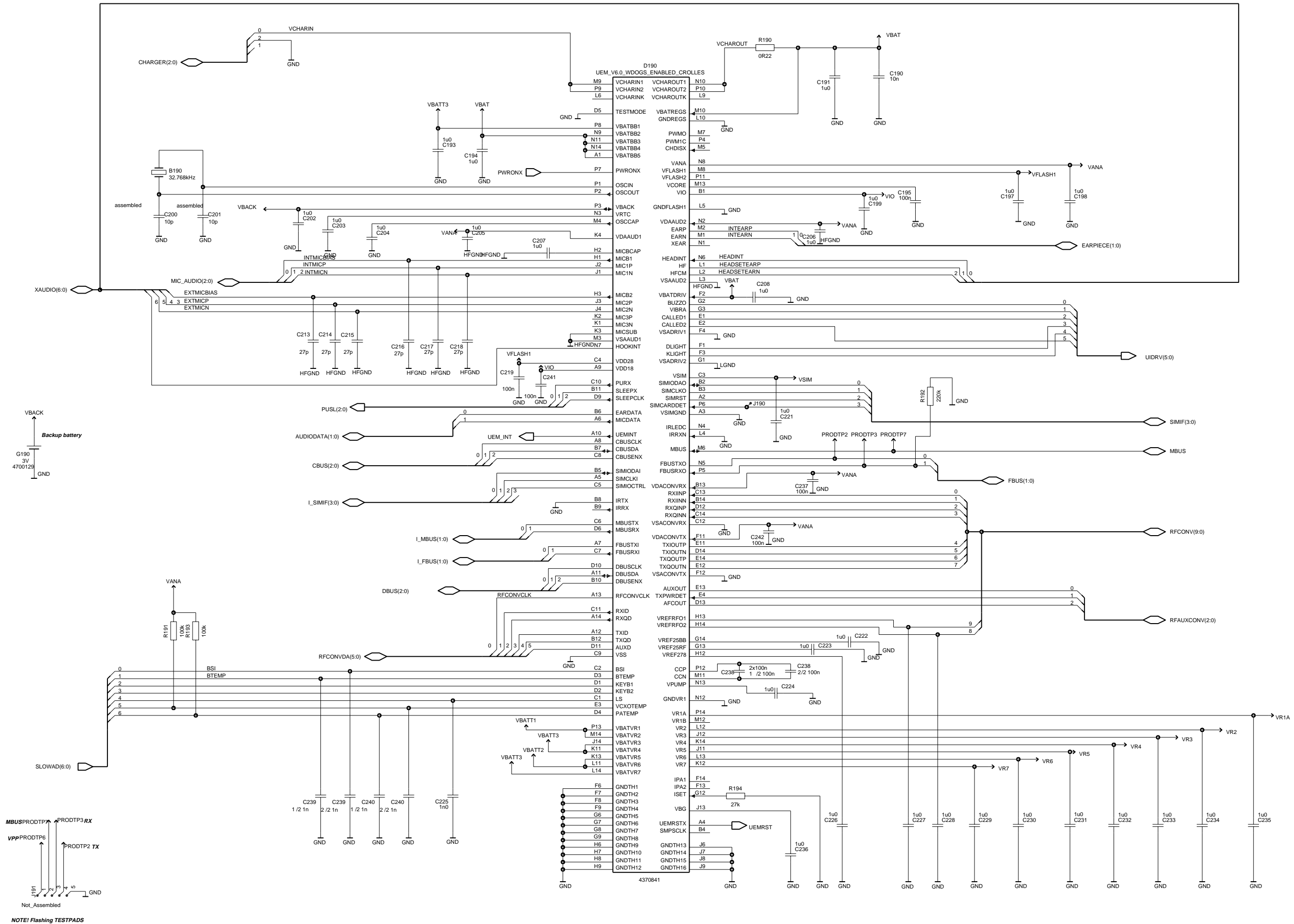


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Name Calypso audio
Assoc

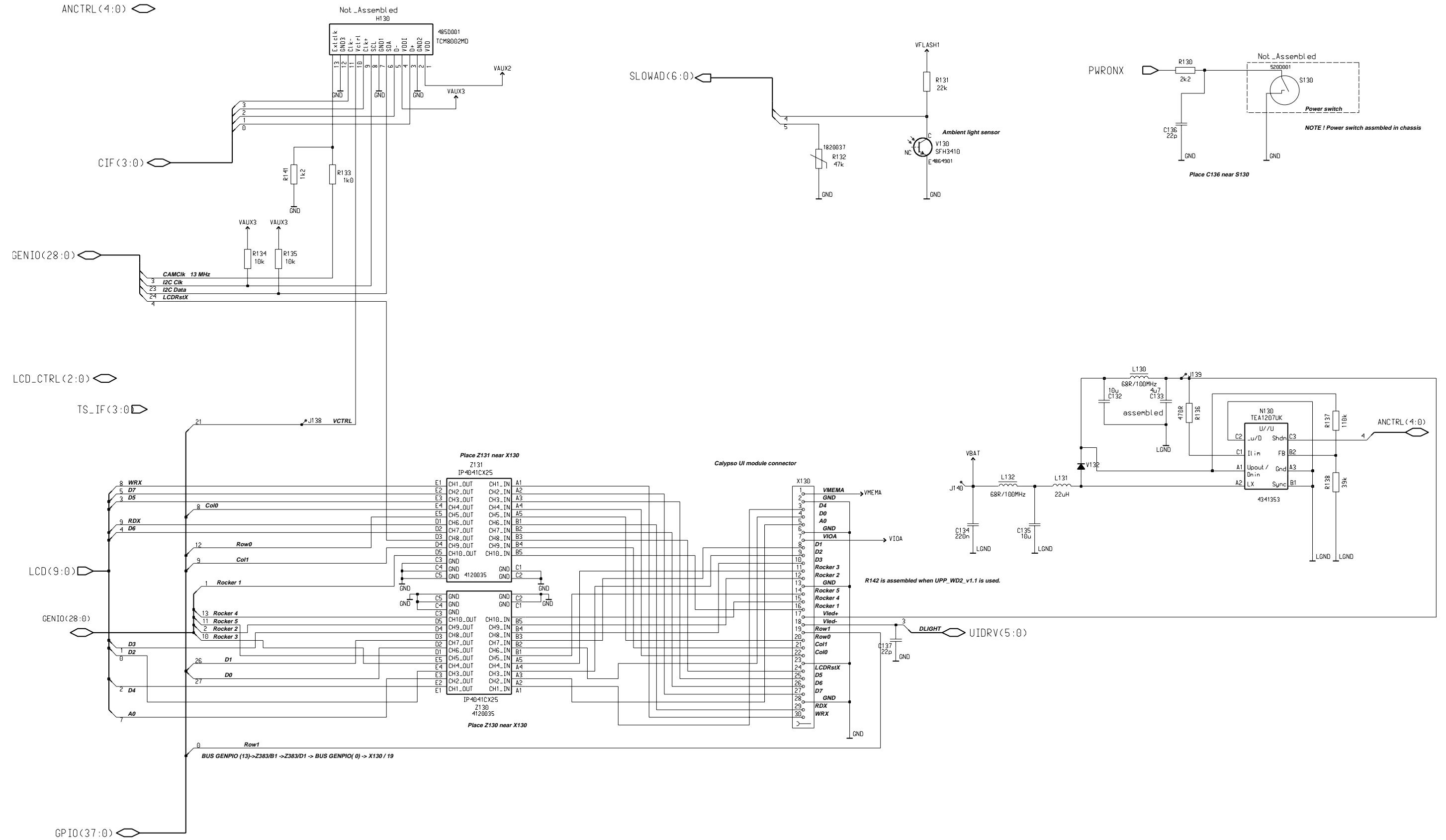
Appr dd-mmm-yy
Des. Jarvinen 06-Oct-2000
r

UEM Diagram

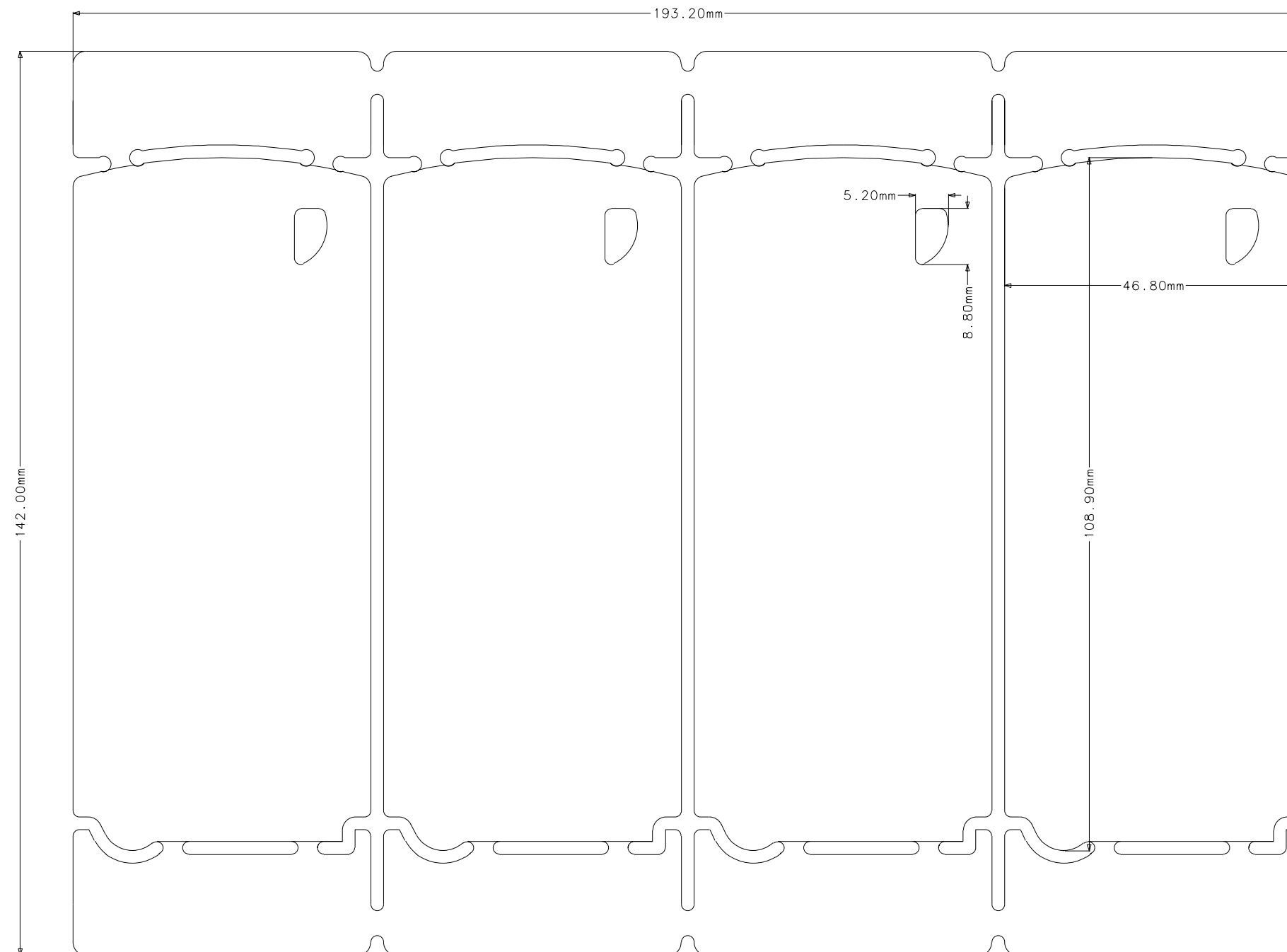


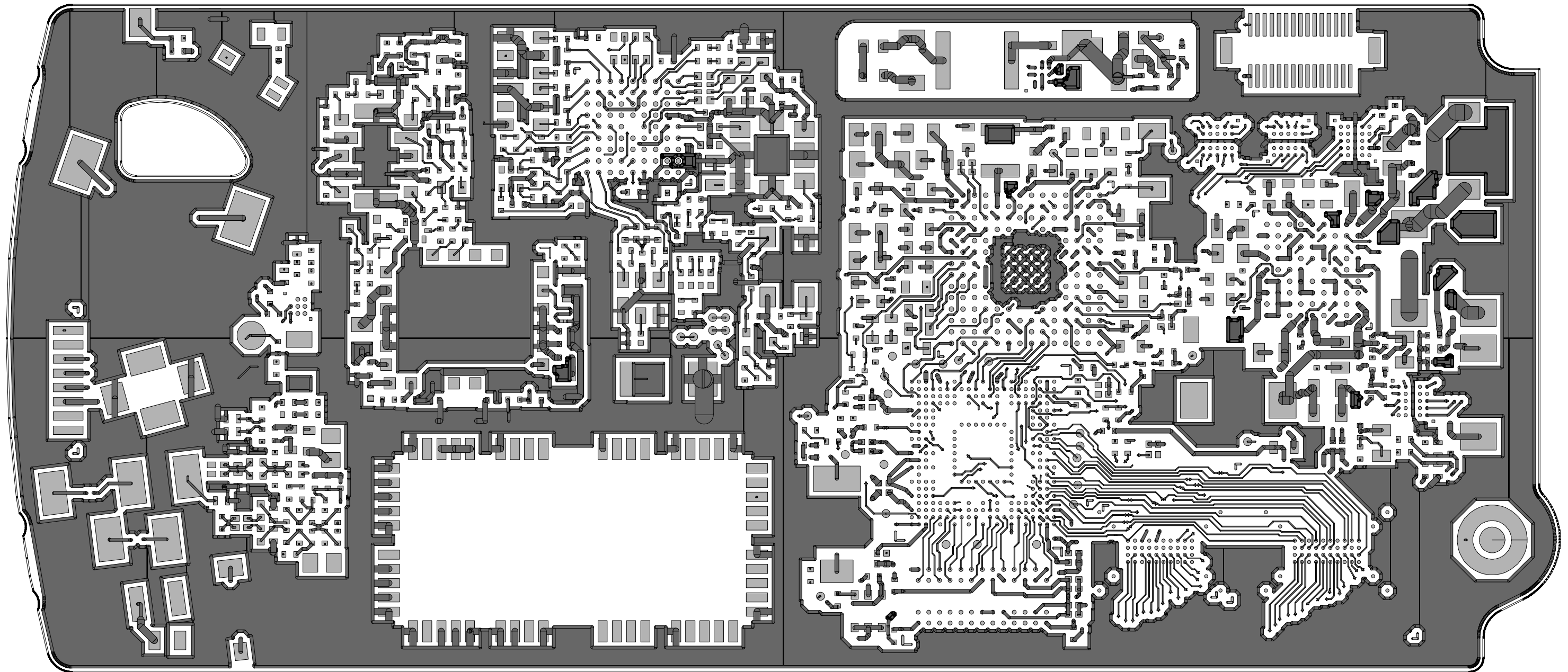
User Interface Diagram

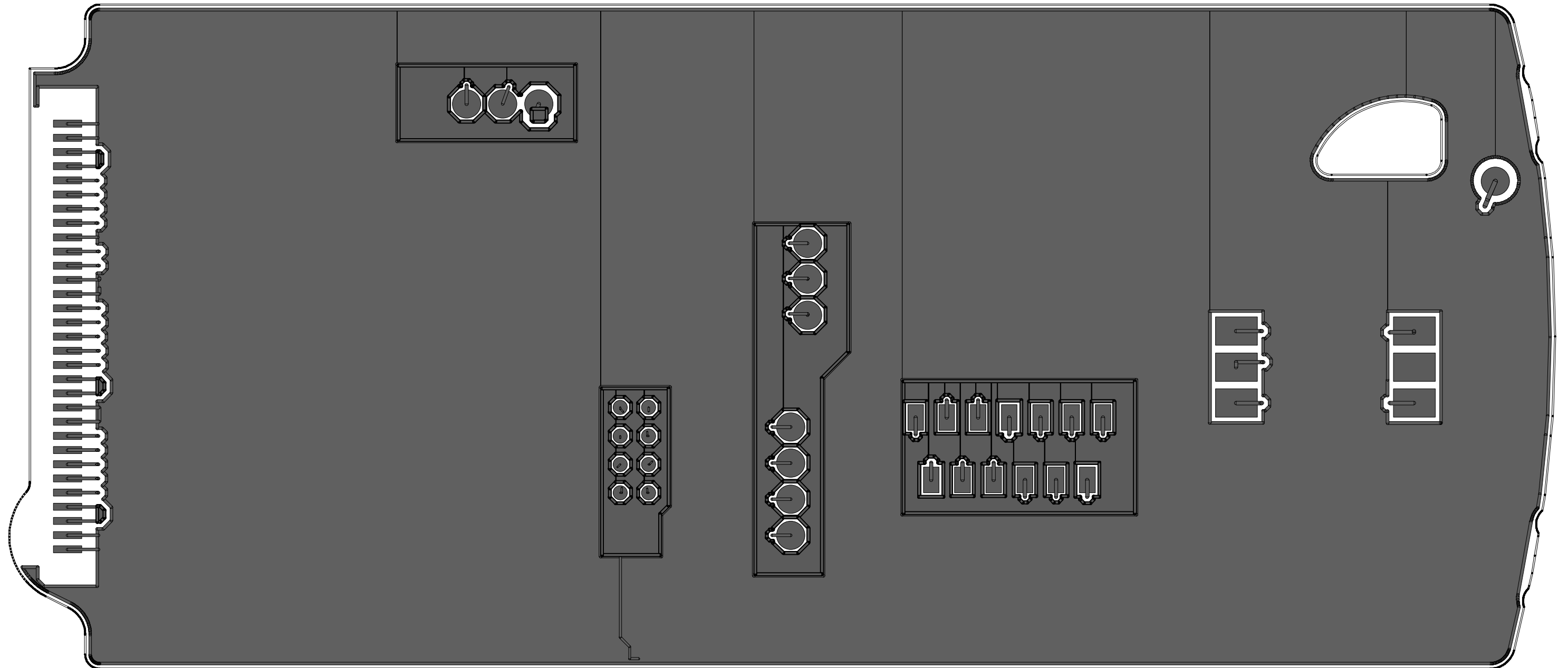
NOTE! Assembled In Camera Shield



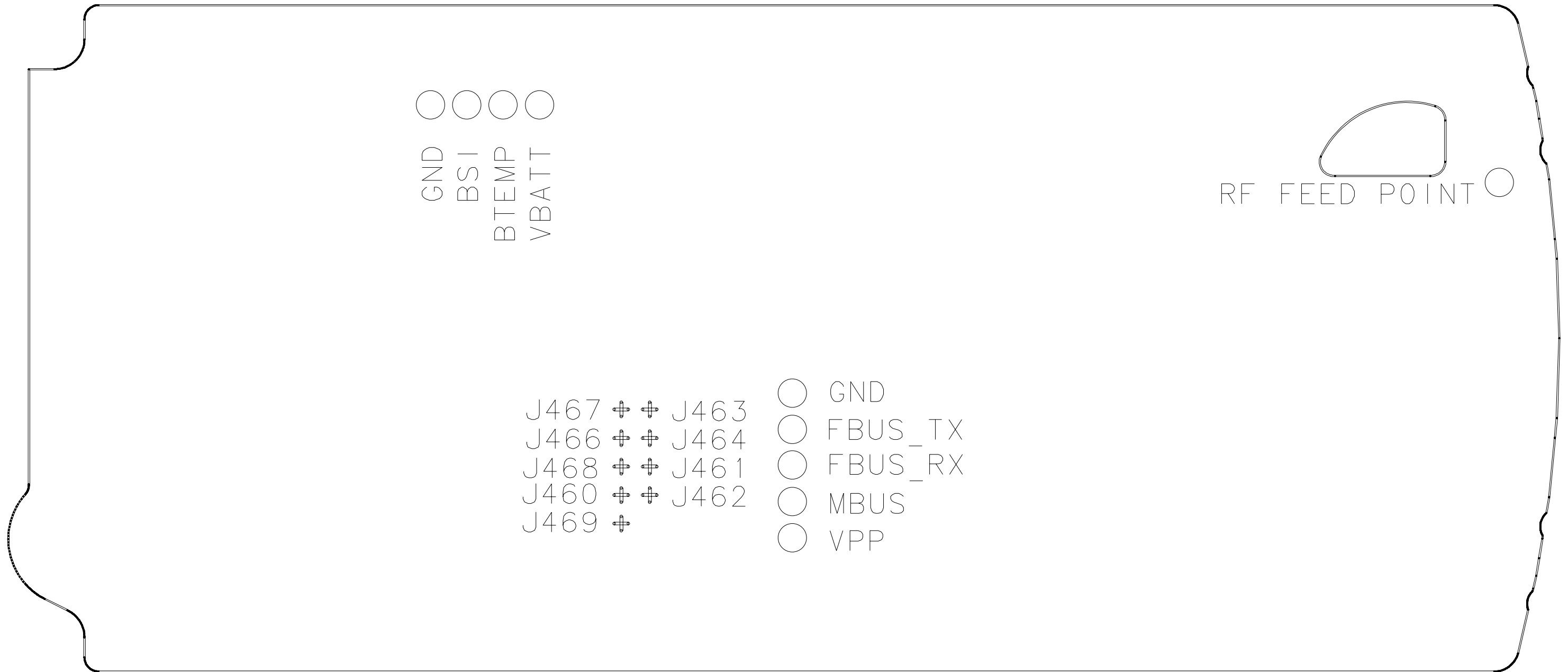
DIM







Testpoints Bottom



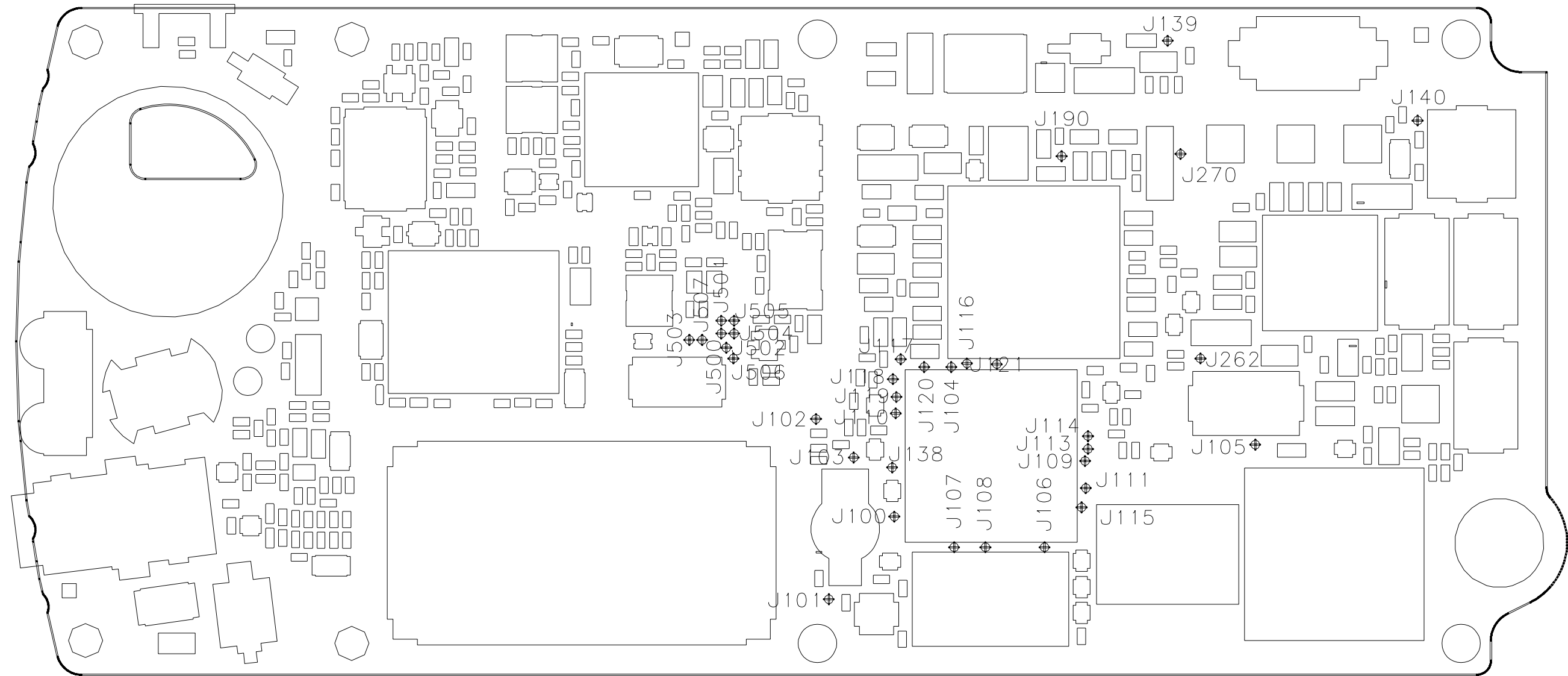
○
○
○
○
GND
BSI
BTEMP
VBATT

J467 † † J463
J466 † † J464
J468 † † J461
J460 † † J462
J469 †

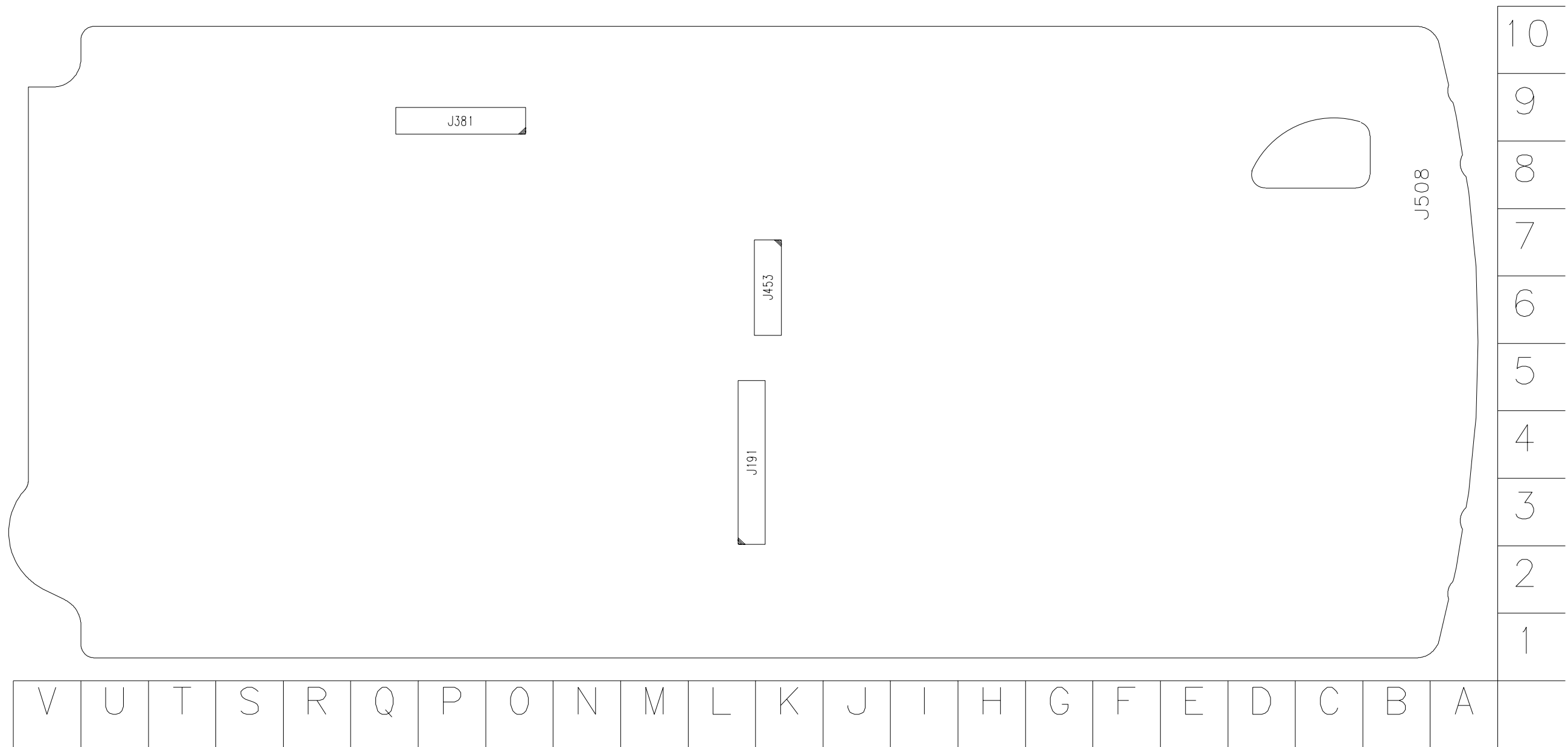
○ GND
○ FBUS_TX
○ FBUS_RX
○ MBUS
○ VPP

RF FEED POINT ○

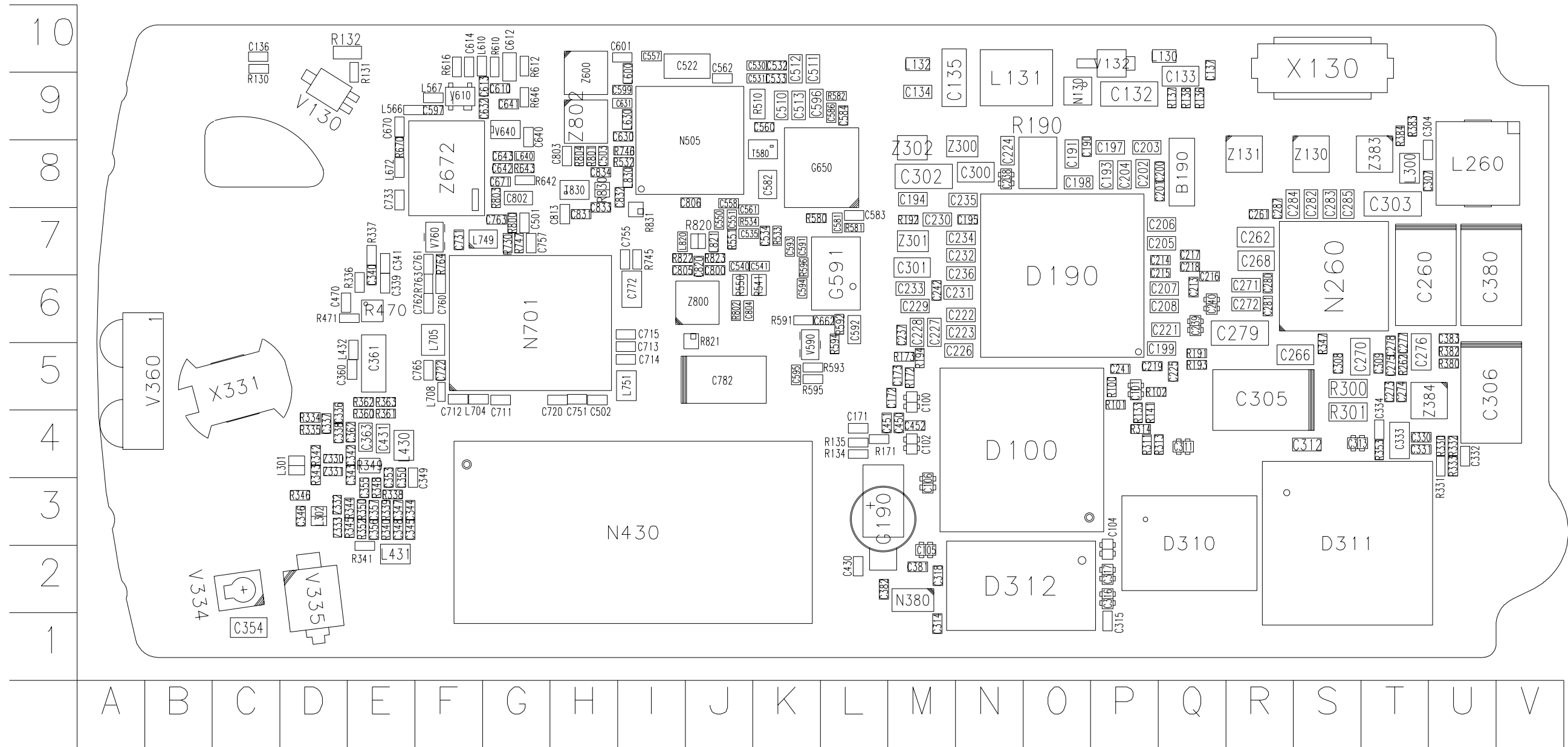
Testpoints Top



ASM Bottom



ASM Top



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